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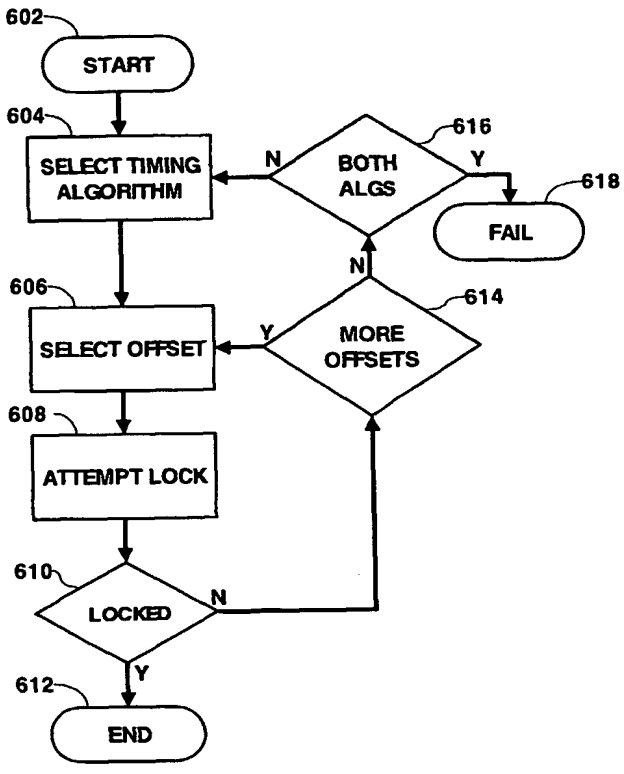
(54) Title: SYMBOL TIMING SEARCH ALGORITHM



(57) Abstract: A system is described for establishing timing synchronism between a local receiver symbol clock and and a transmitter symbol clock. A prescribed number of offset values are calculated for desired symbol timing range, The offset values being grouped substantially symmetrically about a central offset value. Each of the preselected offset values are tested to see if symbol timing recovery (STR) lock can be achieved by starting at the central offset value and gradually moving away from such value (606, 608, 610 and 614). Finally, two timing detection algorithms are used and switched between the two algorithms is carried out as desired to maximize the possibility of STR lock (604).

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